

REMARKS

The examiner has allowed claims 4, 6, 10 and 11 and rejected claims 1-3, 5, 7-9 and 12-13 under 35 U.S.C. §103(a) as being unpatentable over the publication "A Parallel Pipelined DSP Processor Core" by Aikens et al.

In response, applicant hereby cancels claims 1-3, 5, 7-9 and 12-13. It is believed that this application is in condition for allowance, and a notice of allowance of claims 4, 6, 10 and 11 is solicited.

Respectfully submitted,  
by



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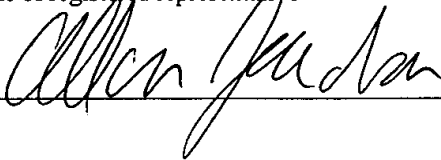
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